

REMARKS/ARGUMENTS

Please enter the amendment under the provisions of 37 CFR 1.116.

Reconsideration and re-examination are hereby requested.

Examiner makes the statement that : "However, Szczepanek disclose corrupting the delayed data if the checking determines a CRC error (col. 6, line 35-38)."

Col. 6, line 35-38 states:

wherein step (b) includes the steps of checking parity bits within said tag header and asserting an error indicator if an error is detected; and wherein step (g) includes the step of corrupting said CRC for said fourth modified frame if said error indicator is asserted.(emphasize added)

Thus, Szczepanek **CORRUPTS THE GENERATED CRC** IN THE PRESENCE OF A DATA PARITY ERROR. Applicant, on the other hand, CORRUPTS THE GENERATED DATA PARITY IN THE PRESENCE OF A CRC ERROR. **Szczepanek does NOT disclose corrupting the delayed data if the checking determines a CRC error at col. 6, lines 35-38 attached.**

Claim 1 points out that the method includes:

corrupting the delayed DATA if such checking **determines a CRC error**, such corruption of the DATA being performed prior to the data words passes to said output; and
checking the output to determine whether there has been a corruption of the delayed DATA.. (emphasis added)

Claim 2 points out that the method includes:

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corrupting the delayed DATA if such checking determines a CRC error, such corruption of the DATA being performed prior to the data words pass to said output;
checking the output to determine whether there has been a corruption of the delayed DATA; and
wherein the corrupting comprising corrupting a parity bit of one such data words. (emphasis added)

In the event any additional fee is required, please charge such amount to Patent and Trademark Office Deposit Account No. 50-0845.

Respectfully submitted,

Date

4-1-04

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Attachments:

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